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## EUROPEAN IBIS SUMMIT MEETING

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**Time/Date:** 13:00 - 18:00, Wednesday, May 16, 2012

**Location:** Parco dei Principi – Sorrento Hotel  
[http://www.royalgroup.it/eng/parco\\_dei\\_principi\\_hotel\\_sorrento/](http://www.royalgroup.it/eng/parco_dei_principi_hotel_sorrento/)  
Via Rota, 1  
80067 Sorrento (NA)  
Italy

Tel. + 39 081 8784644  
Fax + 39 081 8783786

**Room:** SPI 2012 meeting room to be determined

**Content:** Presentations and Discussions

**Purpose:** Solicit and exchange IBIS Model Related information and ideas

**Sponsors:** Micron Technology, Nokia Siemens Networks, Zuken

If your company would be interested in sponsoring this event, please contact Bob Ross [bob@teraspeed.com](mailto:bob@teraspeed.com)

**SPI 2012:** May 13 - 16, 2012

**Location:** Grand Hotel Parco dei Principi – Sorrento, Italy, Italy

**URL:** <http://www.spi2012.org/>

### BACKGROUND:

We have had successful IBIS meetings with the DATE conference for many years, and more recently with the IEEE Workshop on Signal and Power Integrity (formerly Signal Propagation on Interconnects).

This meeting will be conducted as a formal IBIS Summit Meeting. Presentations are expected to be available and archived in an electronic format, and minutes of the meeting will be issued.

The meeting is FREE and OPEN to everyone. Refreshments & coffee breaks are planned.

## CALL FOR PARTICIPANTS:

People involved in IBIS Model development, EDA tool development, and digital circuit design are invited to participate. If you want to participate, please register with the information below:

Name:  
E-mail address:  
Company:  
Telephone:

Send to BOTH:

Antonio Girardi     [agirardi@micron.com](mailto:agirardi@micron.com)  
Lance Wang         [lwang@iometh.com](mailto:lwang@iometh.com)

Deadline:            May 9, 2012

## CALL FOR PRESENTATIONS:

We are seeking presentations from individuals who have IBIS experiences or issues. Some suggested subjects of interest are:

- IBIS Model Development Experiences
- Company IBIS Standards and Requirements
- Generating & Validating IBIS Models
- Future IBIS Developments and Requirements (e.g., IBIS-AMI)
- Modeling of dedicated technology issues (e.g., connectors, and very high speed interfaces)
- EMC/EMI and Power Integrity Issues
- Related modeling issues (e.g., SPICE, S-Parameter modeling)
- Experiences using IBIS in the PCB Design process

Format of Presentation:    LCD Projection  
Time:                            15-30 Minutes

### Electronic Archival:

We require electronic versions for so they can be archived and also made available to non-attendees.

We prefer Power Point or Acrobat formats.

Please send presentations for processing and brief review. Otherwise please bring 20 copies to the meeting.

If you plan a presentation, please supply:

Title:  
Presenter:  
E-mail address:  
Company:  
Telephone:  
Estimated Time:

Send this to BOTH:

Lance Wang      [lwang@iometh.com](mailto:lwang@iometh.com)  
Bob Ross         [bob@teraspeed.com](mailto:bob@teraspeed.com)

Deadline:         May 4, 2012

Note, Vendor-specific or product promotional material is forbidden at IBIS Summits . We strongly advise that the presentations be submitted by the deadline for review. The IBIS Open Forum reserves the right to stop or reject a scheduled presentation that has not been reviewed.

**AGENDA:**

The agenda includes presentations, discussions, breaks and refreshments and will be developed as presentation proposals are received.

**LIST OF NEARBY HOTELS:**

See                To Be Supplied

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