

#### Frequency and Time Domain Variability Analysis of an On-Chip Inverted Embedded Microstrip Line Using a Macromodeling-based Stochastic Galerkin Method

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#### Motivation

- Interconnect example
- New stochastic modeling strategy
- Variability analysis of on-chip IEM line
- Conclusions





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#### Modeling for on-chip interconnect design

- More and more wave effects appearing:
  - Interconnects become electrically long(er)
  - Skin-effect, slow-wave effect (semiconductors), ...
- Ever more stringent design specifications
  - Bandwidth, speed, crosstalk, noise margin, ...

#### Miniaturization

- Manufacturing process introduces randomness
- Position and width, shape of cross-section, ...

#### Designers need accurate modeling tools that allow an efficient variability analysis



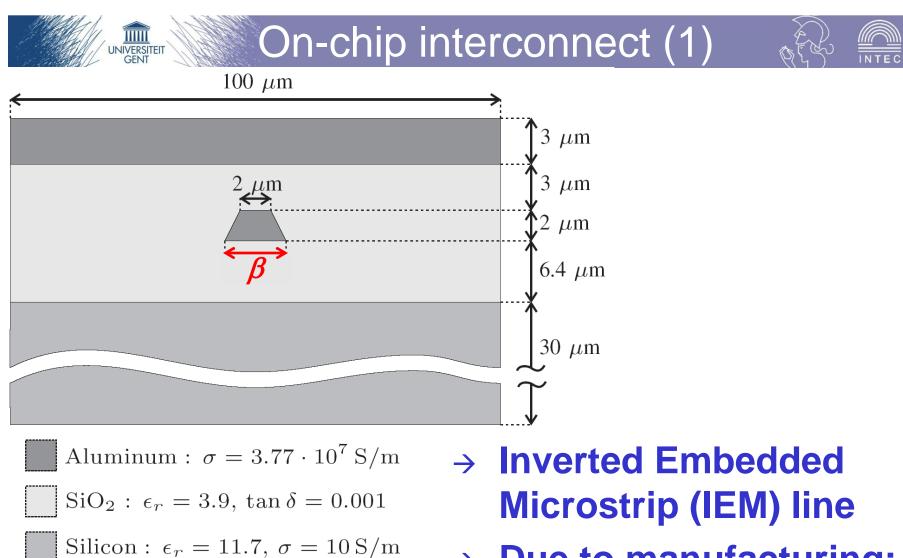


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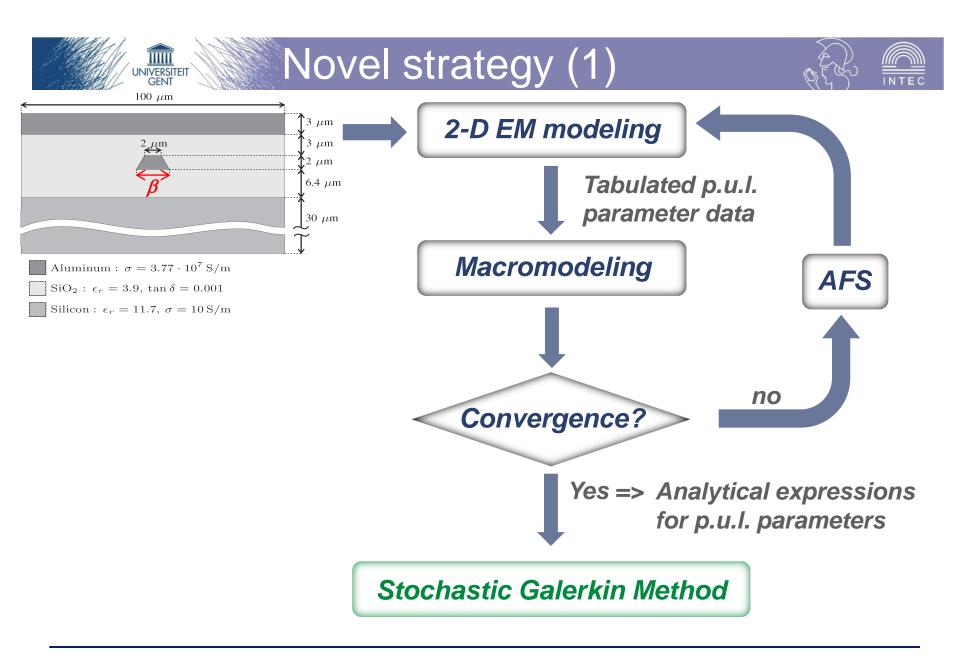
 $\rightarrow \text{ Due to manufacturing:} \\ random parameter \beta$ 





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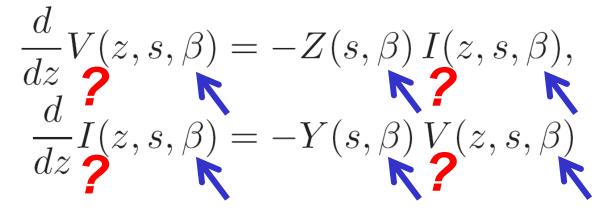






#### STEP 1: 2-D EM modeling

Stochastic Telegrapher's equations (single line):



- V and I: unknown voltages and currents along line
  - Function of position, frequency and of stochastic parameter β
- Z and Y: known p.u.l. transmission line parameters
  - UGent's Dirichlet-to-Neumann (DtN) solver
  - Tabulated data, but very accurate (all loss mechanisms)

#### Novel strategy (3)



#### **STEP 2: Parameterized macromodeling**

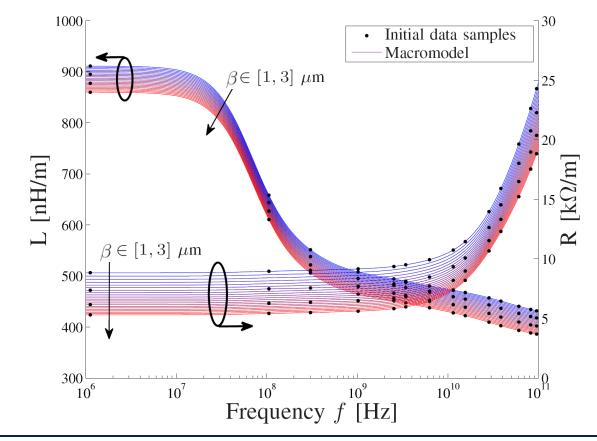
Vector Fitting and Lagrange interpolation

$$Z^{\mathrm{mm}}(s,\beta) = \sum_{v=1}^{V} w_v Z^{\mathrm{umm}}(s,\beta_v) \prod_{\substack{k=1\\k\neq v}}^{V} (\beta - \beta_k),$$
$$Y^{\mathrm{mm}}(s,\beta) = \sum_{v=1}^{V} w_v Y^{\mathrm{umm}}(s,\beta_v) \prod_{\substack{k=1\\k\neq v}}^{V} (\beta - \beta_k).$$

Rational w.r.t. frequency / polynomial w.r.t.  $\beta$ 

- For all on-chip interconnects (no heuristic models)
- Polynomials  $\rightarrow$  convenient choice

# Preliminary result (after step 1 and 2): P.u.l. impedance (Z = R + sL)

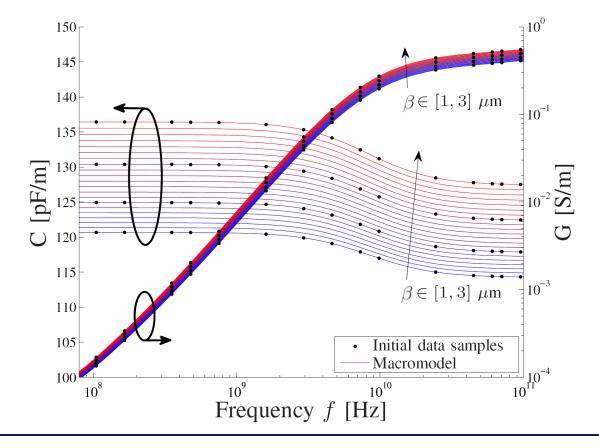


Novel strategy (4)

#### Novel strategy (5)



## Preliminary result (after step 1 and 2) – cont.: P.u.l. admittance (Y = G + sC)

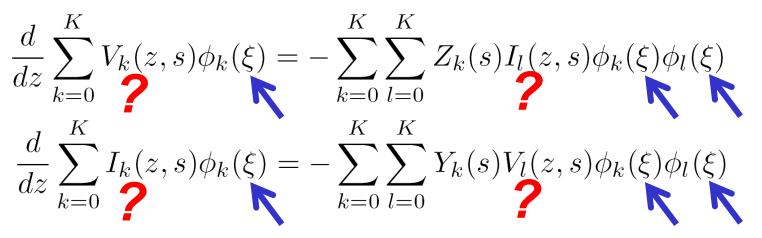


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#### STEP 3: Stochastic Galerkin Method (SGM)

- Assume, e.g., that  $\beta$  is random variable
  - Normalize:  $\beta = \mu_{\beta}(1 + \sigma_{\beta}\xi)$
- SGM: Polynomial Chaos (PC) expansion + Galerkin weighting
  - 1. PC-expanded Telegrapher's equations :



#### Novel strategy (7)



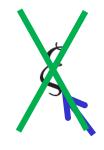
#### STEP 3: Stochastic Galerkin method (SGM) - cont.

2. Galerkin projection, i.e. weighting with same set of polynomials  $\phi_m(\xi), m = 0, \dots, K$ :

 $\forall m = 0, \dots, K$ 

$$\frac{d}{dz}V_{m}(z,s) = -\sum_{k=0}^{K}\sum_{l=0}^{K} \alpha_{klm}Z_{k}(s)I_{l}(z,s)$$

$$\frac{d}{dz}I_{m}(z,s) = -\sum_{k=0}^{K}\sum_{l=0}^{K} \alpha_{klm}Y_{k}(s)V_{l}(z,s)$$
with:  $\alpha_{klm} = \langle \phi_{k}(\xi)\phi_{l}(\xi), \phi_{m}(\xi) \rangle / m!$ 



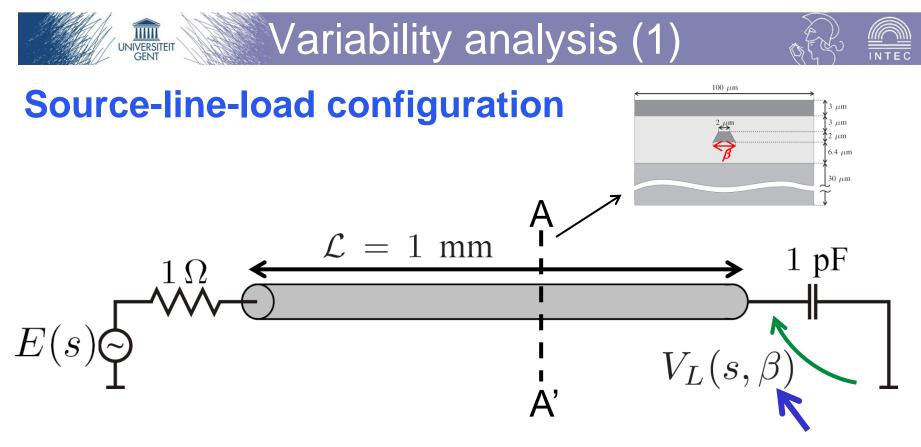
### Thanks to SGM, this is a deterministic matrix ordinary differential equation (ODE)

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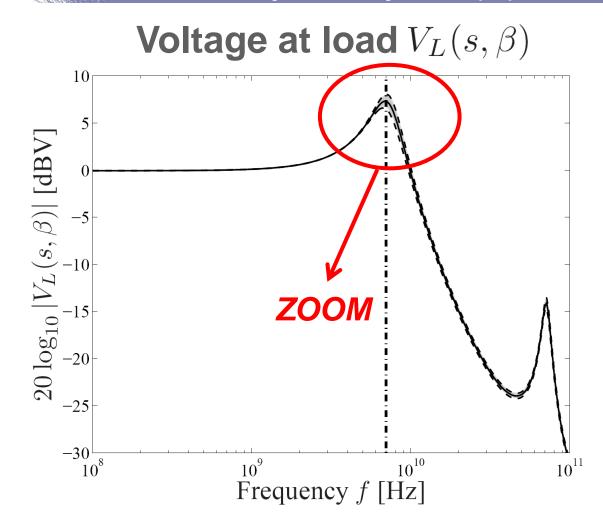


- eta is a Gaussian RV:  $\mu_eta=2~\mu{
  m m}~$  and  $\sigma_eta~=~10\%$
- Post-processing: time domain (transient source)
- Compare with MC run (50000 samples of  $\beta$  )

#### Variability analysis (2)



p. 17

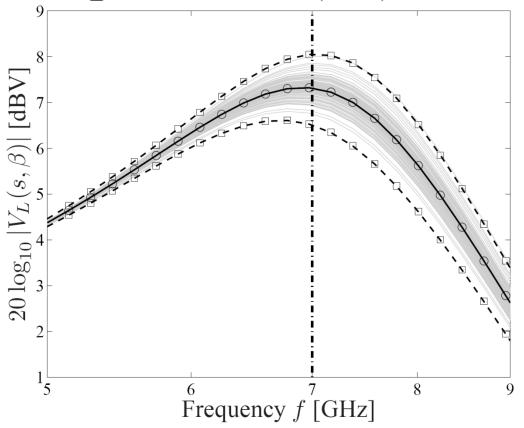


Full: mean  $\mu_{|V_L|}$  (SGM) / Dashed:  $\pm 3\sigma_{|V_L|}$  -variation (SGM) / Gray: MC samples

#### Variability analysis (3)



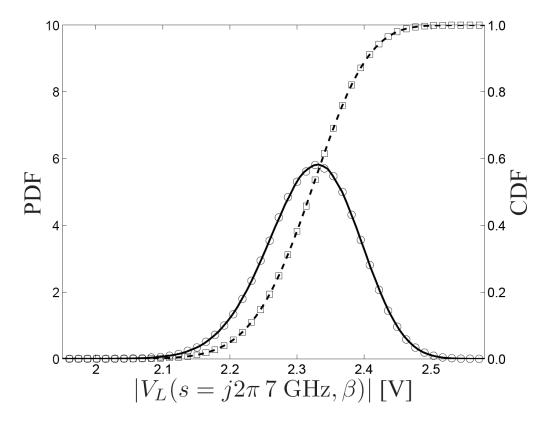
#### Voltage at load $V_L(s,\beta)$ : detail



Full: mean  $\mu_{|V_L|}$  (SGM) / Dashed:  $\pm 3\sigma_{|V_L|}$ -variation (SGM) / Gray: MC samples / Circles: mean  $\mu_{|V_L|}$  (MC) / Squares:  $\pm 3\sigma_{|V_L|}$ -variations (MC) p. 18

#### Variability analysis (4)

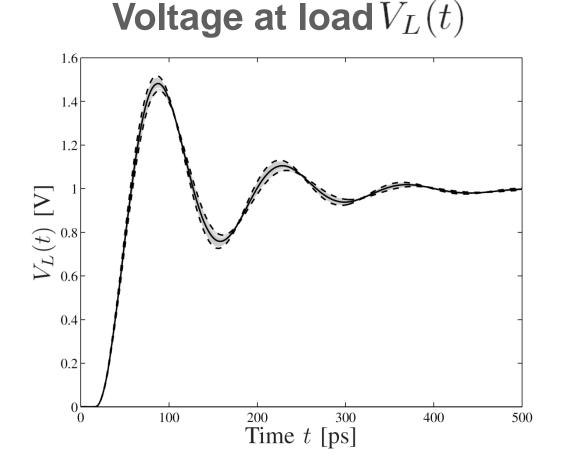
### Probability density function (PDF) and cumulative distribution function (CDF) @ 7 GHz



Full and dashed lines: SGM / Circles and squares: MC



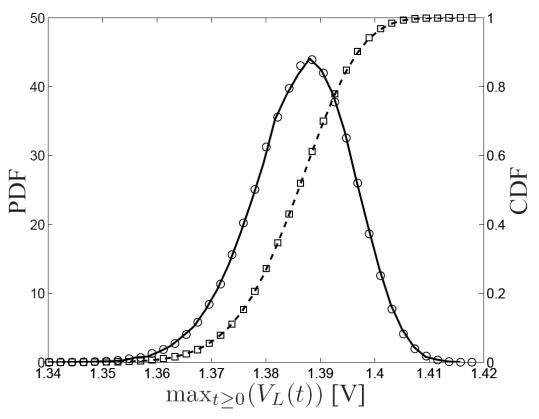




Time-domain voltage source: ramped step (0V -> 1V), 30 ps rise time

# Probability density function (PDF) and

#### cumulative distribution function (CDF) of overshoot



Full and dashed lines: SGM / Circles and squares: MC

#### Variability analysis (7)



#### 2-D EM + macromodeling (offline):

• Accuracy: 0.1%

- CPU time: 140 s
- Efficiency of novel stochastic modeling strategy compared to "tractable MC(\*)":

Technique	CPU time [s]			Speed-up factor
	setup	solve	total	
Novel approach	0.02	0.11	0.13	32
Monte Carlo			4.13	

Note: MC without macromodeling takes about 6 months...

(\*) D. Vande Ginste, D. De Zutter, D. Deschrijver, T. Dhaene, and F. Canavero, "Macromodeling based variability analysis of an inverted embedded microstrip line," *IEEE* 20<sup>th</sup> Conf. on EPEPS, 23-26 Oct. 2011, pp. 153-156





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- State-of-the-art on-chip interconnect design: manufacturing process plays an important role
- New (stochastic) modeling methods needed
- Presented stochastic modeling strategy
  - 2-D EM modeling
  - Macromodeling
  - Stochastic Galerkin Method

#### → First method for *on-chip* interconnects!





#### Tested on on-chip interconnects:

• Single IEM line

- Substrate losses, semiconductors, finite thickness and conductivity of the metallic line, ...
- Influence of manufacturing: trapezoidal cross-section

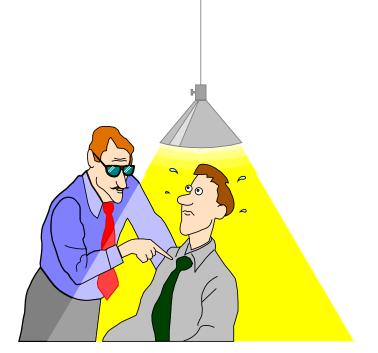
#### **Comparison with MC:**

- Excellent accuracy
- Much improved efficiency

#### • Further reading:

D. Vande Ginste, D. De Zutter, D. Deschrijver, T. Dhaene, P. Manfredi, and F. Canavero, "Stochastic Modeling-Based Variability Analysis of On-Chip Interconnects," *IEEE Trans. on Components, Packaging, and Manufacturing Technology, IEEE Early Access,* 2012





### **Questions?**